

# COMPLEMENTARY SILICON PLANAR EPITAXIAL TRANSISTORS



CSA1020 PNP CSC2655 NPN

TO-92 Plastic Package

### ABSOLUTE MAXIMUM RATINGS (Ta=25°C unless specified otherwise)

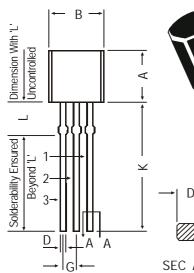
DESCRIPTION	SYMBOL	VALUE	UNIT	
Collector Emitter Voltage	V <sub>CEO</sub>	50	V	
Collector Base Voltage	V <sub>CBO</sub>	50	V	
Emitter Base Voltage	V <sub>EBO</sub>	5	V	
Collector Current	I <sub>C</sub>	2	А	
<b>Collector Power Dissipation</b>	Pc	900	mW	
Operating And Storage Junction Temperature Range	T <sub>j</sub> , T <sub>stg</sub>	-55 to +150	٥C	

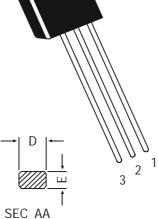
## ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	min	typ	max	UNIT
Collector Emitter Voltage	BV <sub>CEO</sub> I <sub>C</sub> =10mA,I <sub>B</sub> =0		50		-	V
Collector Cut off Current	I <sub>CBO</sub>	$V_{CB}$ =50V, $I_{E}$ = 0	-		1.0	μΑ
Emitter Cut off Current	I <sub>EBO</sub>	$V_{EB} = 5V, I_{C} = 0$	-		1.0	μΑ
DC Current Gain	h <sub>FE</sub>	$V_{CE}=2V,I_{C}=500mA *$	70		240	
		V <sub>CE</sub> =2V,I <sub>C</sub> =1.5A	40		-	
Collector Emitter Saturation Voltage	$V_{CE(sat)}$	$I_{C}$ =1A, $I_{B}$ =50mA	-		0.5	V
Base Emitter Saturation Voltage	V <sub>BE(sat)</sub>	$I_{C}$ =1A, $I_{B}$ =50mA	-		1.2	V
DYNAMIC CHARACTERISTICS						
Gain Bandwidth Product	f <sub>T</sub>	I <sub>C</sub> =500mA, V <sub>CE</sub> =2V	-	100	-	MHz
Output Capacitance	C <sub>ob</sub>	I <sub>E</sub> =0, V <sub>CB</sub> =10V,f=1MHz				
		PNP	-	40	-	РF
		NPN	-	30	-	₽F
Switching Time						
Turn on Time	t <sub>on</sub>	$V_{CC}=30V, I_{B1}=I_{B2}=$	-	0.1	-	us
Storage Time	t <sub>stg</sub>	50mA, $R_L$ =30 $\Omega$	-	1.0	-	us
Fall Time	t <sub>f</sub>	Duty Cycle=1%	-	0.1	-	us
Classification		0	Y			
h <sub>FE</sub> *		70 - 140	120 - 240			

# **TO-92 Plastic Package**

# **TO-92 Plastic Package**





F K	<b>-</b> ∼↓	
$ \begin{array}{c}                                     $	↓	с)

**PIN CONFIGURATION** 

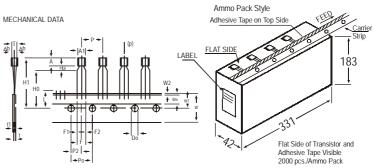
1. BASE

2. COLLECTOR

3. EMITTER

DIM	MIN.	MAX.			
А	4.32	5.33			
В	4.45	5.20			
С	3.18	4.19			
D	0.41	0.55			
Е	0.35	0.50			
F	5 DEG				
G	1.14	1.40			
Н	1.14	1.53			
К	12.70	_			
L	1.982	2.082			

All diminsions in mm.



## **TO-92 Transistors on Tape and Ammo Pack**

#### All dimensions in mm unless specified otherwise

ITEM	0.445.01	SPECIFICATION			DEMADIAG	
II EIVI	SYMBOL	MIN.	NOM.	MAX.	TOL .	REMARKS
BODY WIDTH	A1	4.0		4.8		
BODY HEIGHT BODY THICKNESS	A T	4.8 3.9		5.2 4.2		
PITCH OF COMPONENT	P	3.9	12.7	4.2	±1	
FEED HOLE PITCH	Po		12.7		±0.3	CUMULATIVE PITCH
FEED HOLE CENTRE TO						ERROR 1.0 mm/20 PITCH
COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH
DISTANCE BETWEEN OUTER	F		5.08		+0.6	
COMPONENT ALIGNMENT	∆h		0	1		AT TOP OF BODY
TAPE WIDTH	W		18		±0.5	
HOLD-DOWN TAPE WIDTH HOLF POSITION	Wo W1		6 9		±0.2 +0.7	
HOLE FOSTION			ŕ		-0.5	
HOLD-DOWN TAPE POSITION	W2		0.5		±0.2	
LEAD WIRE CLINCH HEIGHT	Ho		16	23.25	±0.5	
COMPONENT HEIGHT	H1 L			23.25		
FEED HOLE DIAMETER	Do		4		±0.2	
TOTAL TAPE THICKNESS	t			1.2		t1 0.3 - 0.6
LEAD - TO - LEAD DISTANCEF1,	F2		2.54		+0.4	
CLINCH HEIGHT	H2			3	0.1	
PULL - OUT FORCE	(P)	6N				

NOTES
1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.
2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20
PITCHES.

PITCHES.
3. HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.
4. NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.
5. A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.
6. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

# **Packing Detail**

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

CSA1020 CSC2655

TO-92 Plastic Package

## Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-279 5290, 5141 1119 email@cdil.com www.cdilsemi.com

CSA1020/CSC2655/25072001

Continental Device India Limited

This datasheet has been downloaded from:

www.DatasheetCatalog.com

Datasheets for electronic components.